

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: )  
Nagarajan et al. for ) Group Art Unit: 2185  
INTEL Corporation (Assignee) )  
Serial No.: 10/591,933 ) Examiner: Arpan P. Savla  
Filed: September 6, 2006 )

## For: USING TRANSACTED WRITES AND CACHING MECHANISM TO IMPROVE WRITE PERFORMANCE IN MULTI-LEVEL CELL FLASH MEMORY

## AMENDMENT

Mail Stop: **AMENDMENT**  
COMMISSIONER FOR PATENTS  
P.O.BOX 1450  
ALEXANDRIA, VA 22313-1450

Dear Sir:

In response to the Office Action mailed March 17, 2009, please reconsider the above-identified patent application in view of the remarks and amendments below.